

N-channel 100 V 5 m $\Omega$  standard level MOSFET with improved SOA in TO220 package

11 July 2014

**Product data sheet** 

### 1. General description

Standard level N-channel MOSFET with improved SOA in a TO220 package. Part of Nexperia "NextPower Live" portfolio, the PSMN4R8-100PSE is robust enough to withstand substantial in-rush and fault condition currents during turn on/off, whilst offering a low  $R_{DS(on)}$  characteristic to keep temperatures down and efficiency up in continued use. Ideal for telecommunication systems based on 48 V backplanes / supply rails.

#### 2. Features and benefits

- Enhanced safe operating area (SOA) for superior protection during linear mode operation
- Very low R<sub>DS(on)</sub> for low conduction losses

## 3. Applications

- Electronic fuse
- Hot-swap / Soft-start
- Uninterruptible power supplies
- Motor control

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### 4. Quick reference data

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Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> ≥ 25 °C; T <sub>j</sub> ≤ 175 °C		-	-	100	V
I <sub>DM</sub>	peak drain current	pulsed; $T_{mb}$ = 25 °C; $t_p \le 10 \ \mu$ s; Fig. 3		-	-	693	А
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <u>Fig. 1</u>		-	-	405	W
Static characte	eristics	·					
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C; Fig. 12		-	4.3	5	mΩ
Dynamic chara	acteristics						_
Q <sub>GD</sub>	gate-drain charge	$V_{GS}$ = 10 V; I <sub>D</sub> = 25 A; V <sub>DS</sub> = 50 V;		-	59	83	nC
Q <sub>G(tot)</sub>	total gate charge	<u>Fig. 14; Fig. 15</u>		-	196	278	nC

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Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Avalanche ruggedness							
E <sub>DS(AL)S</sub>	non-repetitive drain- source avalanche energy	$V_{GS}$ = 10 V; $T_{j(init)}$ = 25 °C; $I_D$ = 120 A; $V_{sup}$ ≤ 100 V; $R_{GS}$ = 50 Ω; unclamped; Fig. 4		-	-	542	mJ

## 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	D
2	D	drain		
3	S	source		G-UTA
mb	D	mounting base; connected to drain		mbb076 S
			TO-220AB (SOT78)	

## 6. Ordering information

Table 3. Ordering in	formation		
Type number	Package		
	Name	Description	Version
PSMN4R8-100PSE	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

## 7. Marking

Table 4. Marking codes	
Type number	Marking code
PSMN4R8-100PSE	PSMN4R8-100PSE

## 8. Limiting values

#### Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> ≥ 25 °C; T <sub>j</sub> ≤ 175 °C	-	100	V

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Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>DGR</sub>	drain-gate voltage	$T_j \ge 25 \text{ °C}; T_j \le 175 \text{ °C}; R_{GS} = 20 \text{ k}\Omega$		-	100	V
V <sub>GS</sub>	gate-source voltage			-20	20	V
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <u>Fig. 1</u>		-	405	W
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>j</sub> = 25 °C; <u>Fig. 2</u>	[1]	-	120	Α
		V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 100 °C; <u>Fig. 2</u>	[1]	-	120	Α
I <sub>DM</sub>	peak drain current	pulsed; $t_p \le 10 \ \mu s$ ; $T_{mb} = 25 \ ^\circ C$ ; Fig. 3		-	693	Α
T <sub>stg</sub>	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
T <sub>sld(M)</sub>	peak soldering temperature			-	260	°C
Source-dra	in diode					
I <sub>S</sub>	source current	T <sub>mb</sub> = 25 °C	[1]	-	120	А
I <sub>SM</sub>	peak source current	pulsed; $t_p \le 10 \ \mu s$ ; $T_{mb} = 25 \ ^\circ C$		-	693	А
Avalanche	ruggedness					
E <sub>DS(AL)S</sub>	non-repetitive drain-source avalanche energy	$V_{GS}$ = 10 V; $T_{j(init)}$ = 25 °C; $I_D$ = 120 A; $V_{sup}$ ≤ 100 V; $R_{GS}$ = 50 Ω; unclamped; Fig. 4		-	542	mJ

[1] Continuous current limited by package.

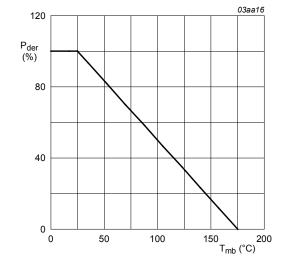


Fig. 1. Normalized total power dissipation as a function of mounting base temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

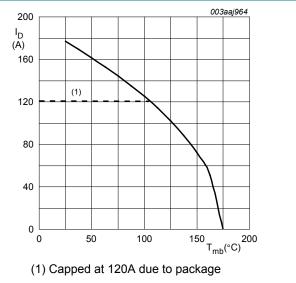
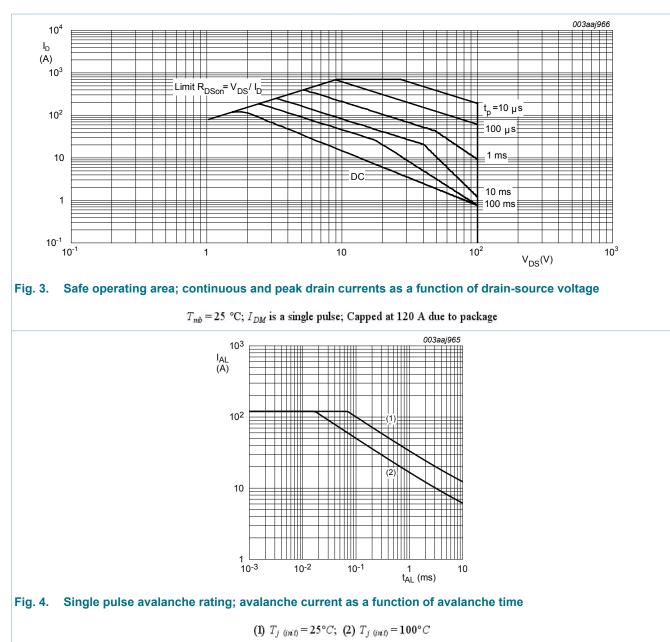


Fig. 2. Continuous drain current as a function of mounting base temperature

 $V_{GS} \ge 10V$ 

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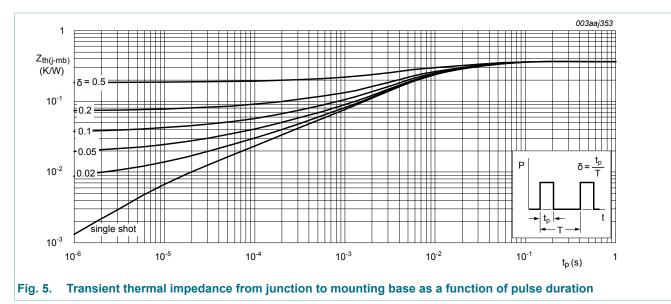




## 9. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	<u>Fig. 5</u>	-	0.3	0.37	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	Minimum footprint; mounted on a printed circuit board	-	60	-	K/W



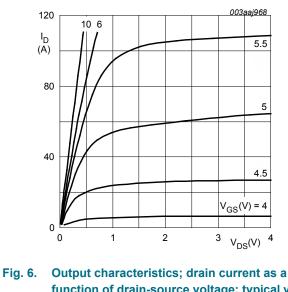


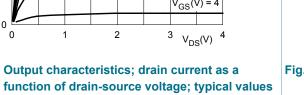
## **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics	1				
V <sub>(BR)DSS</sub>	drain-source	$I_D$ = 250 µA; $V_{GS}$ = 0 V; $T_j$ = 25 °C	100	-	-	V
	breakdown voltage	$I_D$ = 250 µA; $V_{GS}$ = 0 V; $T_j$ = -55 °C	90	-	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	I <sub>D</sub> = 1 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C; Fig. 10; Fig. 11	2	3	4	V
V <sub>GSth</sub>	gate-source threshold voltage	I <sub>D</sub> = 1 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 175 °C; Fig. 11	1	-	-	V
		I <sub>D</sub> = 1 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = -55 °C; Fig. 11	-	-	4.6	V
I <sub>DSS</sub> drain leaf	drain leakage current	$V_{DS}$ = 100 V; $V_{GS}$ = 0 V; $T_j$ = 25 °C	-	0.16	10	μA
		$V_{DS}$ = 100 V; $V_{GS}$ = 0 V; $T_j$ = 175 °C	-	-	500	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = -20 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	10	100	nA
		$V_{GS}$ = 20 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	10	100	nA
Doon	drain-source on-state resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C; Fig. 12	-	4.3	5	mΩ
		V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 100 °C; Fig. 13; Fig. 12	-	-	9	mΩ
		V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 175 °C; Fig. 12; Fig. 13	-	-	13.5	mΩ
R <sub>G</sub>	gate resistance	f = 1 MHz	0.43	0.85	1.7	Ω

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Dynamic cl	haracteristics	· · · · · ·	I			
Q <sub>G(tot)</sub>	total gate charge	$I_D$ = 25 A; $V_{DS}$ = 50 V; $V_{GS}$ = 10 V; Fig. 14; Fig. 15	-	196	278	nC
		$I_D = 0 \text{ A}; V_{DS} = 0 \text{ V}; V_{GS} = 10 \text{ V}$	-	166.9	234	nC
Q <sub>GS</sub>	gate-source charge	$I_D$ = 25 A; $V_{DS}$ = 50 V; $V_{GS}$ = 10 V;	-	40	56	nC
Q <sub>GD</sub>	gate-drain charge	Fig. 14; Fig. 15	-	59	83	nC
V <sub>GS(pl)</sub>	gate-source plateau voltage	I <sub>D</sub> = 25 A; V <sub>DS</sub> = 50 V; <u>Fig. 14</u> ; <u>Fig. 15</u>	-	4.3	-	V
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 50 V; V <sub>GS</sub> = 0 V; f = 1 MHz; T <sub>j</sub> = 25 °C; <u>Fig. 16</u>	-	10665	14400	pF
C <sub>oss</sub>	output capacitance		-	674	910	pF
C <sub>rss</sub>	reverse transfer capacitance		-	459	643	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 50 V; R <sub>L</sub> = 2 Ω; V <sub>GS</sub> = 10 V;	-	41	61.5	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 4.7 \Omega$	-	65	97.5	ns
t <sub>d(off)</sub>	turn-off delay time		-	127	190.5	ns
t <sub>f</sub>	fall time		-	69	103.5	ns
Source-dra	in diode	· · · ·	I			
V <sub>SD</sub>	source-drain voltage	$I_{S}$ = 25 A; $V_{GS}$ = 0 V; $T_{j}$ = 25 °C; <u>Fig. 17</u>	-	0.79	1.2	V
t <sub>rr</sub>	reverse recovery time	$I_{\rm S}$ = 25 A; dI_{\rm S}/dt = -100 A/µs; V <sub>GS</sub> = 0 V;	-	72	94	ns
Q <sub>r</sub>	recovered charge	V <sub>DS</sub> = 50 V	-	227	296	nC





 $T_j = 25^{\circ}C$ 

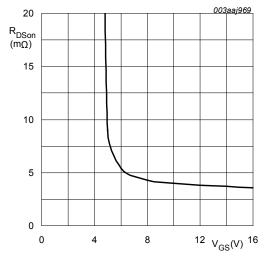


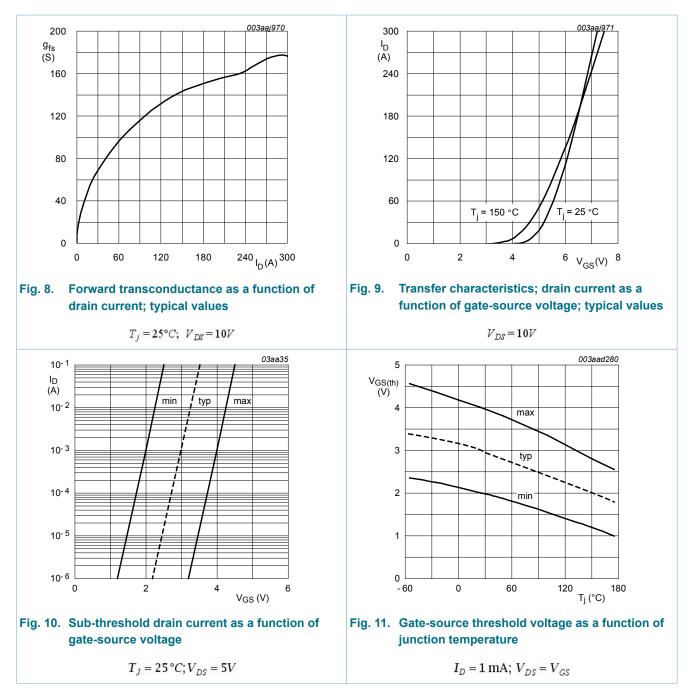
Fig. 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

 $T_j = 25^{\circ}C; \ I_D = 25A$ 

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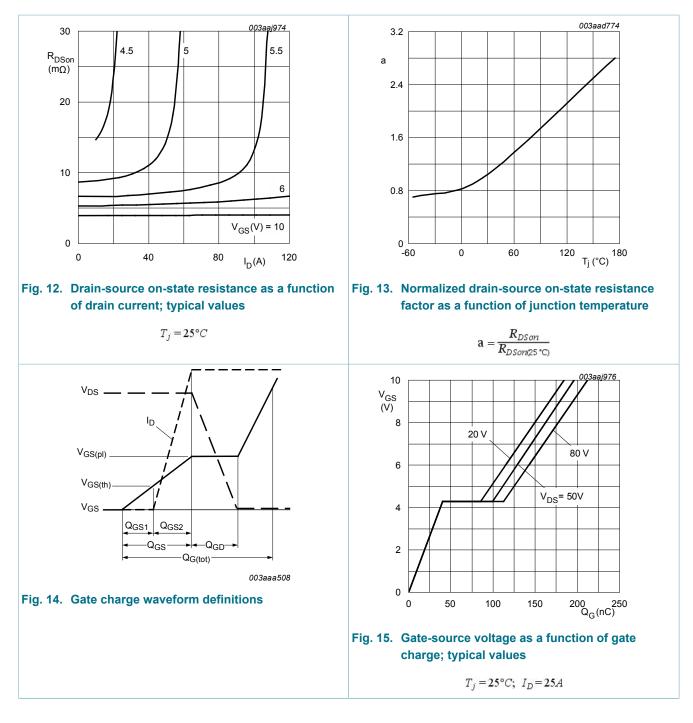
## PSMN4R8-100PSE





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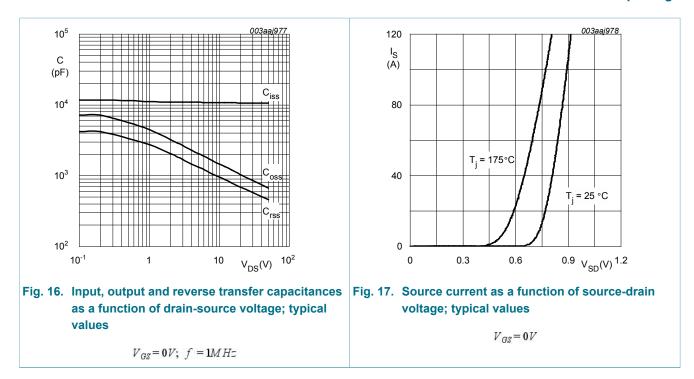




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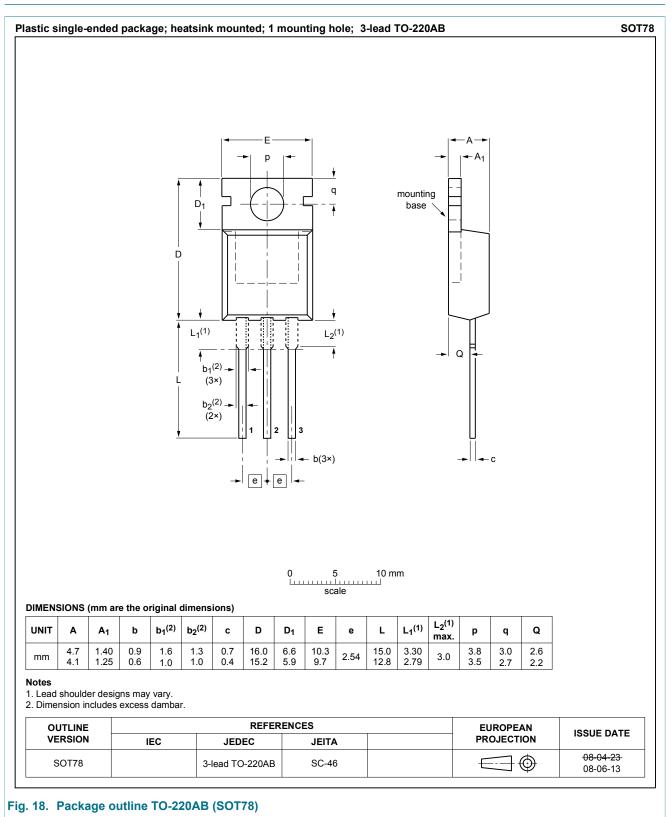
## PSMN4R8-100PSE

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## 11. Package outline



#### PSMN4R8-100PSE

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#### N-channel 100 V 5 mΩ standard level MOSFET with improved SOA in TO220 package

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