

## PCN / EOL Notification

PCN Number: CC141705A (Revised 3/17/16) Notification Date\*: May 28, 2014 See Changes in Blue Text

Table 1		
Parameter/Feature	AT24C04C	AT24C04D
Operating Voltage	1.7V to 5.5V	1.7V to 3.6V
Operating Temperature	-40°C to +85°C	-40°C to +85°C
Endurance	1,000,000 cycles (Page Mode, +25°C, 3.3V)	1,000,000 cycles (Byte or Page Mode, +25°C, 1.7V to 3.6
Data Retention	100 years	100 years
Supply Current, Read	0.4mA typ (5.0V, 100kHz) 1.0mA max (5.0V, 100kHz)	0.08mA typ (1.8V, 400kHz) 0.3mA max (1.8V, 400kHz) 0.15mA typ (3.6V, 1MHz) 0.5mA max (3.6V, 1MHz)
Supply Current, Write	2.0mA typ (5.0V, 100kHz) 3.0mA max (5.0V, 100kHz)	0.2mA typ (3.6V, 1MHz) 1.0mA max (3.6V, 1MHz)
Standby Current	1μΑ max (1.7V) 6μΑ max (5.5V)	0.08μA typ (1.8V) 0.4μA max (1.8V) 0.1μA typ (3.6V) 0.8μA max (3.6V)
Maximum Clock Frequency	1MHz (2.5V min.) 400kHz (1.7V min.)	1MHz (2.5V min.) 400kHz (1.7V min.)
Clock Pulse Width Low	1.2 $\mu$ s min (f <sub>SCL</sub> = 400kHz) 0.4 $\mu$ s min (f <sub>SCL</sub> = 1MHz)	1.3 $\mu$ s min (f <sub>SCL</sub> = 400kHz) 0.5 $\mu$ s min (f <sub>SCL</sub> = 1MHz)
Clock Pulse Width High	0.6 $\mu$ s min (f <sub>SCL</sub> = 400kHz) 0.4 $\mu$ s min (f <sub>SCL</sub> = 1MHz)	$0.6\mu s$ min ( $f_{SCL} = 400 kHz$ ) $0.4\mu s$ min ( $f_{SCL} = 1 MHz$ )
Input Filter Noise Suppression	100ns max ( $f_{SCL} = 400$ kHz) 50ns max ( $f_{SCL} = 1$ MHz)	100ns max ( $f_{SCL} = 400$ kHz) 100ns max ( $f_{SCL} = 1$ MHz)
Clock Low to Data Out Valid	900ns max ( $f_{SCL} = 400$ kHz) 550ns max ( $f_{SCL} = 1$ MHz)	900ns max ( $f_{SCL} = 400$ kHz) 450ns max ( $f_{SCL} = 1$ MHz)
Bus Free Time Between Start and Stop	1.2 $\mu$ s min (f <sub>SCL</sub> = 400kHz) 0.5 $\mu$ s min (f <sub>SCL</sub> = 1MHz)	1.3 $\mu$ s min (f <sub>SCL</sub> = 400kHz) 0.5 $\mu$ s min (f <sub>SCL</sub> = 1MHz)
Input Rise Time	300ns max ( $f_{SCL} = 400$ kHz) 300ns max ( $f_{SCL} = 1$ MHz)	300ns max ( $f_{SCL} = 400 \text{kHz}$ ) 100ns max ( $f_{SCL} = 1 \text{MHz}$ )
Input Fall Time	300ns max ( $f_{SCL} = 400$ kHz) 100ns max ( $f_{SCL} = 1$ MHz)	300ns max ( $f_{SCL} = 400$ kHz) 100ns max ( $f_{SCL} = 1$ MHz)
Write Cycle Time	5ms max	5ms max
Page Write Size	16 bytes max	16 bytes max
Full Array Hardware Write Protect	Yes	Yes

## **Identification Method to Distinguish Change:**

Different catalog part numbers for AT24C04C and AT24C04D exist. Please refer to the respective datasheets for part marking schemes for each package type.

## Table 2

Below is a part number cross reference for the AT24C04C and AT24C04D families. Special CAN (customer specific) part numbers created for the AT24C04C that are not listed in the table below remain in production:

5.5V Part Number	3.6V Part Number	Package	Carrier Type
AT24C04C-PUM	AT24C04D-PUM	PDIP	Bulk
AT24C04C-SSHM-B	AT24C04D-SSHM-B	JEDEC SOIC	Bulk
AT24C04C-SSHM-T	AT24C04D-SSHM-T	JEDEC SOIC	T/R, 4K per reel
AT24C04C-XHM-B	AT24C04D-XHM-B	TSSOP	Bulk
AT24C04C-XHM-T	AT24C04D-XHM-T	TSSOP	T/R, 5K per reel
AT24C04C-MAHM-T	AT24C04D-MAHM-T	UDFN	T/R, 5K per reel
AT24C04C-MAHM-E	AT24C04D-MAHM-E	UDFN	T/R, 15K per reel
AT24C04C-STUM-T	AT24C04D-STUM-T	SOT23	T/R, 5K per reel
AT24C04C-CUM-T	AT24C04D-CUM-T	VFBGA	T/R, 5K per reel
AT24C04C-WWU11M	AT24C04D-WWU11M	Wafer Sales	
AT24C04C-WWU27M	AT24C04D-WWU27M	Wafer Sales	

Qualification Data:		☐ Will be available:	□ N/A
Samples:	Available now. Please contact Atmel Sales to submit Sample Request Form (samples in tape format only)	☐ Will be available	□ N/A

## Quantifiable Impact on Quality & Reliability:

No impact. AT24C04D is form, fit, and function of AT24C04C for 1.7 to 3.6V.

Forecasted Availability Date: AT24C04C – already available

AT24C04D - already available

Last Time Buy Date: November 26, 2014

Last Ship Date: May 26, 2015

\*All orders placed after the notification date are non-cancellable and non-returnable (NCNR).

**Atmel Contact:** Please contact your Atmel Sales Representative or Distributor for additional information (when replying via e-mail please include the PCN number in subject line).

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